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Original Patent

Patentees: Alex Kalnitsky
Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION
TECHNIQUE FOR AN
INTEGRATED CIRCUIT

Issued: November 16, 1999

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Reissue Application

Applicants: Alex Kalnitsky
Yih-Shung Lin

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INTEGRATED CIRCUIT

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Certificate of Mailing

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Argie Rodriguez
Signature

AMENDMENT UNDER RULE 312

DATE: September 28, 2006

TO THE COMMISSIONER FOR PATENTS:

Please amend the above-referenced patent application as follows:

Amendments to the Claims begins on page 3 of this paper.

Remarks/Arguments begin on page 9 of this paper.